

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,110,263 B2  
APPLICATION NO. : 10/797753  
DATED : September 19, 2006  
INVENTOR(S) : He et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Figures 2A & 2B  
[Ref no.] 200 should be deleted.

Figure 5  
[Ref no.] 560 should be deleted.

Figure 7  
[Ref no.] 724 should be deleted.

Column 3  
Line 8, "...dielectric 270..." should read --...dielectric layer 270...--.  
Line 43, "...traces 314..." should read --...signal traces 324 344...--.

Column 4  
Line 4, "...the slot width..." should read --...the slot width 434...--.  
Line 10-11, "...the inner layers..." should read --...the inner layers 570...--.

Column 5  
Lines 43-46, "8. ...located alone..." should read --8. ...located along...--.

Column 6  
Lines 37, "...the a assembly..." should read --...the assembly...--.

Signed and Sealed this

Fifteenth Day of July, 2008



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*